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109-706728
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		Subclass
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ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/706728	F	380 713	193	2131 2132	Colin T. Shainin

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2136

TIT E

Architecture of an encryption circuit implementing various types of encryption algorithms simultaneously without a loss of performance

PTO-2040
12/99

Best Available Copy

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
	(Assistant Examiner)	(Date)		Amount Due	Date Paid
	(Primary Examiner)	(Date)		ISSUE BATCH NUMBER	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.					
	(Legal Instruments Examiner)	(Date)			

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